



JPW

Patent  
Attorney's Docket No. H1484

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shibly S. Ahmed et al. ) Group Art Unit: 2815  
Application No.: 10/614,001 ) Examiner: J. Fenty  
Filed: July 8, 2003 )  
For: SELECTIVE SILICIDATION OF )  
GATES IN SEMICONDUCTOR )  
DEVICES TO ACHIEVE )  
MULTIPLE THRESHOLD VOLTAGES )

**INFORMATION DISCLOSURE STATEMENT**  
**TRANSMITTAL LETTER**

U.S. Patent and Trademark Office  
220 20<sup>th</sup> Street S.  
Customer Window, Mail Stop Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Sir:

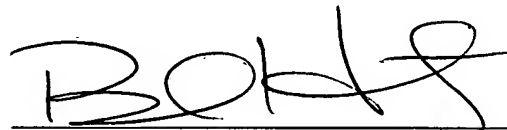
Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for  
the above-identified patent application.

- ☒ No additional fee for submission of the IDS is required.
- ☐ The fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☒ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ Charge \$ \_\_\_\_\_ to Deposit Account No. 50-1070 for the fee due.
- ☐ A check in the amount of \$ \_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R.  
§§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to  
Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By:   
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Customer Number: 26615

Date: June 14, 2004



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**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)**

U.S. Patent and Trademark Office  
220 20<sup>th</sup> Street S.  
Customer Window, Mail Stop Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202  
Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), applicant(s) bring(s) to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed after the events recited in Section 1.97(b) but, to the undersigned's knowledge, before the mailing date of either a Final Action or a Notice of Allowance. Under the provisions of 37 C.F.R. § 1.97(c), this Information Disclosure Statement:

- ☒ includes a certification as specified by Section 1.97(e).
- ☐ is accompanied by a fee of \$180.00 as specified by Section 1.17(p).

☐ **Certification 1:** Each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

☒ **Certification 2:** No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing date of the Information Disclosure Statement.

☒ Copies of the listed documents are attached.

☐ Copies of the listed documents were previously submitted in a prior application, serial no. \_\_\_\_\_, filing date \_\_\_\_\_, upon which applicant(s) rely(ies) for the benefits provided in 35 U.S.C. § 120. Applicant(s) respectfully request(s) that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

☐ The following is a concise statement of relevance of the non-English language documents.

1. \_\_\_\_\_ discloses \_\_\_\_\_.

2. \_\_\_\_\_ discloses \_\_\_\_\_.

☐ English translations of the non-English documents are enclosed.

☐ In lieu of a statement of relevance or translation of the non-English documents, an English language version of a search report from the \_\_\_\_\_ Patent Office in a corresponding application citing these documents and setting forth the relevance thereof is enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claims in the application and applicant(s) determine(s) that the cited document(s) do not constitute "prior art" under United States law, applicant(s) reserve(s) the right to present to the office the relevant facts and law regarding the appropriate status of such documents.


Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If any copending application(s) is/are cited on the attached PTO 1449, the Examiner's attention is directed to the foregoing application(s) in compliance with § 2001.06(b) of the Manual of Patent Examining Procedure. By identifying the copending application(s), the assignee and/or applicant of the application(s) do not waive confidentiality of the application(s). Accordingly, the U.S. Patent and Trademark Office is requested to maintain the confidentiality of the copending application(s) under 35 U.S.C. § 122.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

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Customer Number: 26615

Date: 6/14/2004

**INFORMATION  
DISCLOSURE  
CITATION**  
PTO-1449Customer Number  
**26615**ATTORNEY'S DKT No.  
H1484APPLICATION No.  
10/614,001APPLICANT(S)  
Shibly S. Ahmed et al.FILING DATE  
July 8, 2003GROUP  
2815**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

Copy of co-pending U.S. Patent Application Serial No. 10/857,931, filed June 2, 2004, titled: "Selective Channel Implantation for Forming Semiconductor Devices with Different Threshold Values," Haihong Wang et al., 21page specification, 20 sheets of drawings.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).